

[ELECTRO–MAGNETIC INTERFERENCE PROTECTION CIRCUIT AND METHOD FOR A CLOCK BUFFER]

Abstract

The present invention provides an electro–magnetic interference protection circuit for a clock buffer. The present invention not only uses a ferrite bead that is serially connected with the power source of the clock buffer to protect the clock buffer from electro–magnetic interference. The ferrite bead also connects in parallel with a capacitor to reduce the distortion of a clock signal outputting by the clock buffer due to the ferrite bead, thereby making the communication devices work properly.